EXPRESS MAIL

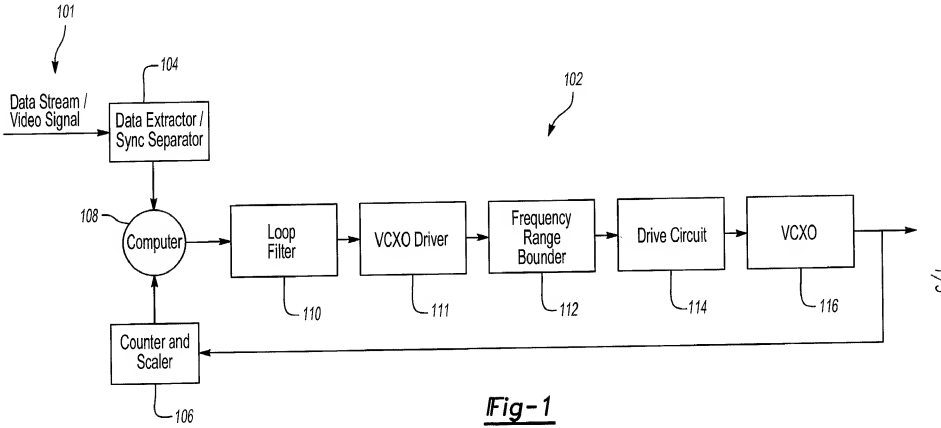

Fig-1

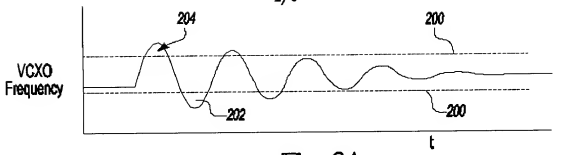

Fig-2A

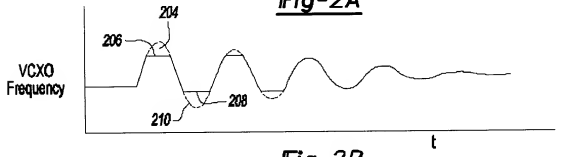

Fig-2B

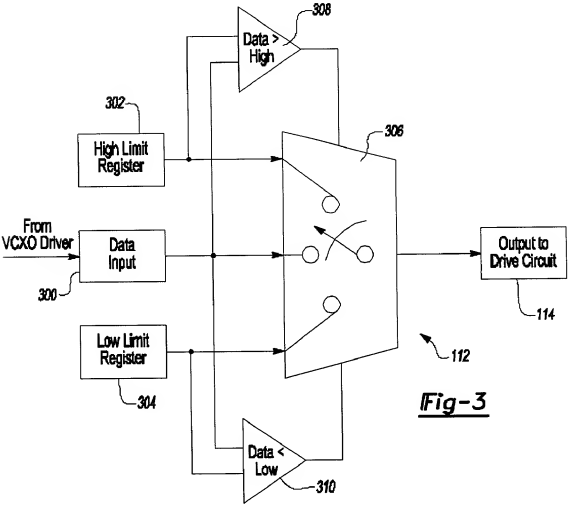

Fig-3

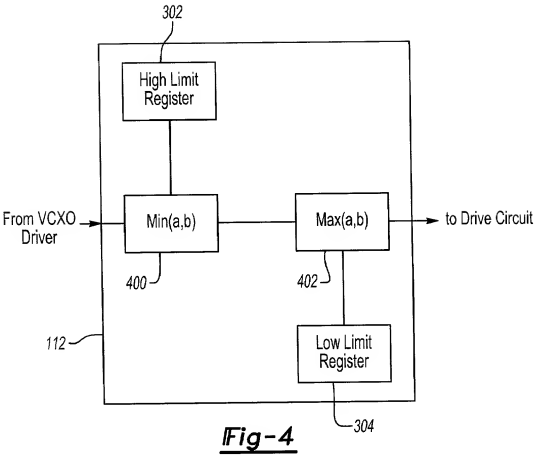
302
High Limit Register
From VCXO Driver
Min(a,b)
Max(a,b)
to Drive Circuit
400
402
112
Low Limit Register
304

Fig-4

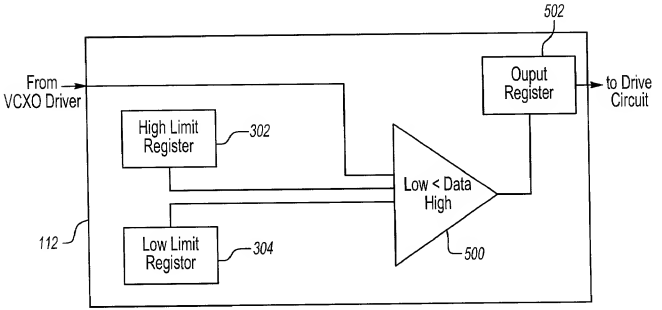
502
From VCXO Driver
Ouput Register
to Drive Circuit
High Limit Register
302
Low < Data High
112
Low Limit Registor
304
500

Fig-5